AGENDA (9/08)

- REMINDER: HW DUE TODAY → FOLLOW NAMING CONVENTIONS
- REMINDER: PARTICIPATION BONUS: WED + TODAY
- RECAP
- FINISH MOSFET CAPACITANCE
- LAYOUT → CROSS-SECTION PRACTICE
  - INDIVIDUALS DRAW
  - WHERE ARE THE R & C’S?
  - VOLUNTEER
Introduction to VLSI

Bob Peddenpohl, University of Kentucky; Modeling Manager, Cypress Semiconductor
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9/08/06 LECTURE
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