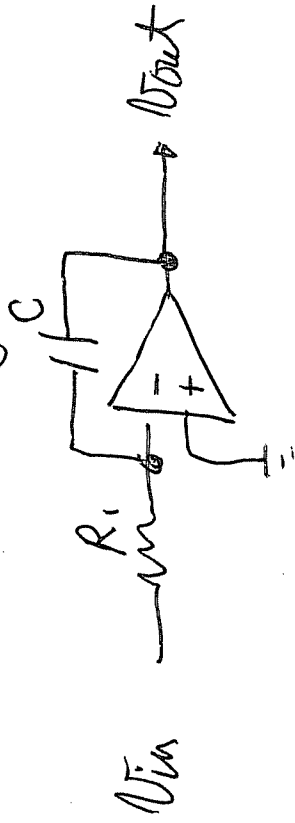


Lecture 14B EES11

①

Integrate and Dump
 Consider an analog integrator



$$\begin{aligned} \frac{V_{out}}{V_{in}} &= -\frac{Z_f}{Z_i} = -\frac{1/sC}{R_1} \\ &= -\frac{1}{sR_1C} = -\frac{1}{j2\pi f R_1 C} \\ &= -\frac{1}{j\omega R_1 C} \end{aligned}$$

Practical Integrator or Low Pass Filter



$$\frac{R_2/sC}{R_2 + 1/sC}$$

$$\frac{V_{out}}{V_{in}} = -\frac{R_2/sC}{R_2 + 1/sC}$$

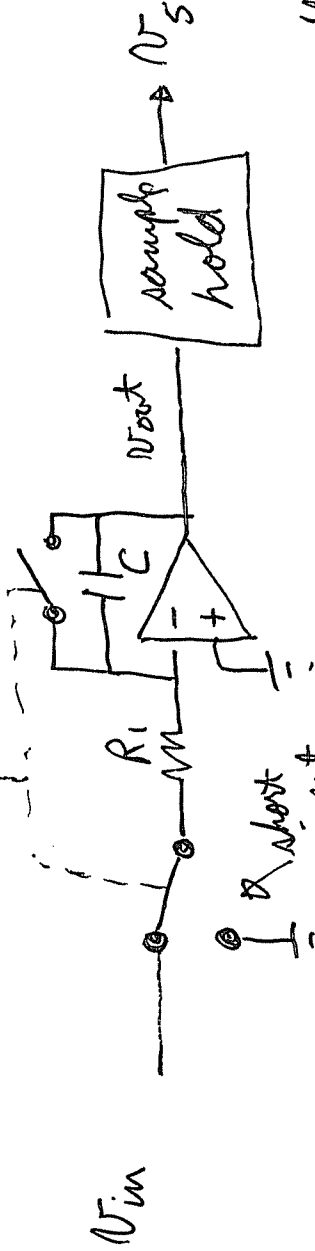
$$= -\frac{R_2/R_1}{1 + j\omega R_2 C}$$

If $R_2 \gg R_1$, then

$$H(\omega) \approx -\frac{1}{j\omega R_1 C}$$

Integrate and dump

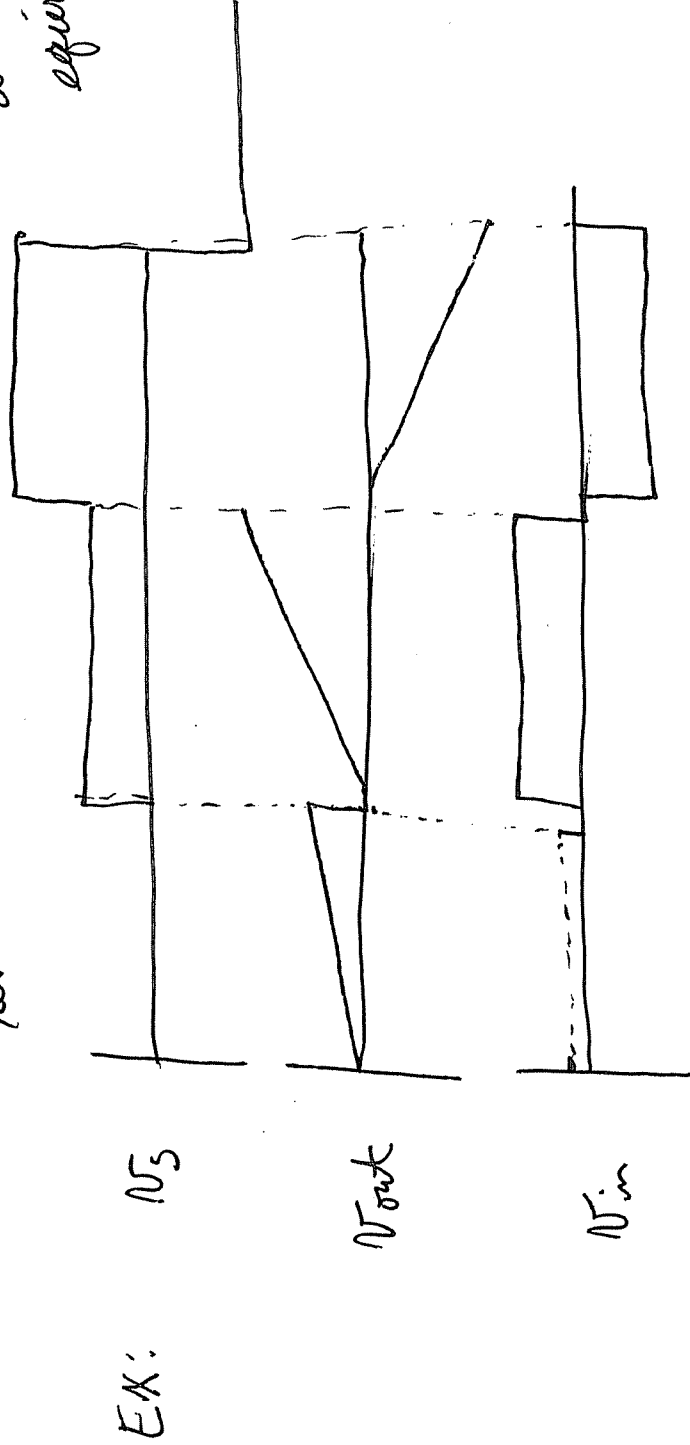
②



I_i short input during reset

$$y_T = \int_0^T g(x) dx$$

where y_T is equivalent to NS



EX: NS

V_{out}

V_{in}

quality (3-26b) in-
 ere may be an
 255 PCM. Alter-
 -sample than com-
 it rate of $R = 32$
 ompanded PCM.
 bit quantization at
 ina and Modena;
 ion and 16 ksam-
 dwidth. A detailed
 signal present, the
 in the prediction
 the scope of this
 topic [Flanagan et

case of DPCM in
 ase of $M = 2$, the
 the function of the
 is. For the case of
 e bit long. The cost
 ie analog-to-digital
 This is the main at-
 ost may be further
 as an RC low-pass
 actor and two-level
 V_c (binary). In this
 h the delta modula-
 eform is illustrated.
 e beginning of each
 † Here the integra-
 that the integrator

$$(3-81)$$

$< 2B_{channel}$, where B_{in} is
 ower limit prevents alias-
 er. (See Example 3-5 for

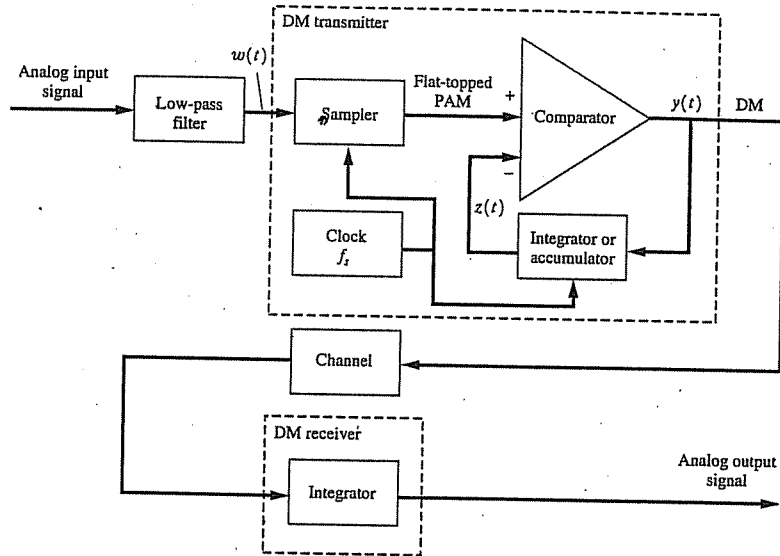


Figure 3-31 DM system.

where $y_i = y(iT_s)$ and δ is the accumulator gain or step size. The corresponding DM output waveform is shown in Fig. 3-32b.

At the receiver, the DM signal may be converted back to an analog signal approximation to the analog signal at the system input. This is accomplished by using an integrator for the receiver that produces a smoothed waveform corresponding to a smoothed version of the accumulator output waveform that is present in the transmitter (Fig. 3-32a).

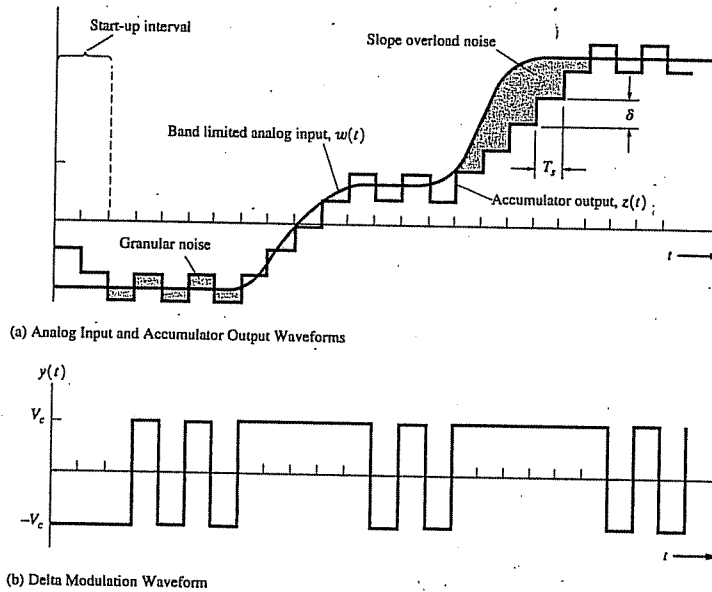


Figure 3-32 DM system waveforms.