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a. Professional Preparation

Sichuan University Software Engineering, B.E. (June 2008).
University of Nebraska-Lincoln Computer Science, M.S. (Dec 2010).
University of Nebraska-Lincoln Computer Science, Ph.D. (Aug 2014).

b. Appointments

08/2014 – present Assistant Professor, Department of Computer Science, University of Kentucky, Lexington, KY.

c. Products

i) Relevant Products

1. T. Yu, Z. Huang, and C. Wang, “ConTesa: Directed Test Suite Augmentation for Concurrent Software”, *IEEE Transactions on Software Engineering (TSE)*, 2018, to appear.
2. T. Yu, W. Wen, X. Han, and J. H. Hayes, “ConPredictor: Concurrency Defect Prediction in Real-World Applications”, *Transactions on Software Engineering (TSE)*, vol. PP, no. 99, pp. 1-1, 2018.
3. T. Yu, “SimEvo: Testing Evolving Multi-Process Software Systems”, *Proceedings of International Conference on Software Maintenance and Evolution (ICSME)*, Shanghai, China, 2017.
4. L. Xiao, T. Yu, “Ripple: A Test-aware Architecture Modeling Framework”, *In International Workshop on Establishing a Community-Wide Infrastructure for Architecture-Based Software Engineering (ECASE)*, co-located with ICSE, Buenos Aires, Argentina, 2017, pp. 14-20.
5. T. Yu, W. Srisa-an, and G. Rothermel, “SimRT: An Automated Framework to Support Regression Testing for Data Races”, the *Proceedings of IEEE International Conference on Software Engineering (ICSE)*, Hyderabad, India, May 31-June 7, 2014, pp. 48-59.

ii) Additional Products

1. X. Han, T. Yu, and D. Lo, “PerfLearner: Learning from Bug Reports to Understand and Generate Performance Test Frames”, *In Proceedings of International Conference on Automated Software Engineering (ASE)*, Montpellier, France, September 3 - 7, 2018, pp. 17-28.
2. T. Yu, T. Zaman, C. Wang, “DESCRY: Reproducing System-level Concurrency Failures”, *In Proceedings of International Symposium on the Foundations of Software Engineering (FSE)*, Paderborn, Germany, pp. 694-704, 2017.
3. T. Yu, X. Qu and M. B. Cohen, “VDTest: An Automated Framework to Support Testing for Virtual Devices”, *In Proceedings of ACM/IEEE International Conference on Software Engineering (ICSE)*, Austin, TX, May 14 - 22, 2016, pp. 583-594.

4. T. Yu, M. Pradel, “SyncProf: Detecting, Localizing, and Optimizing Synchronization Bottlenecks”, *International Symposium of Software Testing and Analysis (ISSTA)*, Saarbrcken, Germany, July 18-20, 2016, pp. 389-400.
5. T. Yu, W. Wen, and J. H. Hayes, “CoLUA: Automatically Predicting Configuration Bug Reports and Extracting Configuration Options”, the *International Symposium on Software Reliability Engineering (ISSRE)*, Ottawa, Canada, October 23-April 27, 2016, pp. 150-161.

d. Synergistic Activities

System Construction: SimExplorer: A VM-based Observable and Controllable Testing Framework.

Service: Organizing Committee: Ada workshop (Co-organizer, co-located with ISSTA) TAV/CPS-IoT workshop (Organizer, co-located with ISSTA) RET 2017 workshop (Program Co-chair, co-located with RE); TECPS 2017 workshop (Organizer, co-located with ISSTA); ASE 2015 (Local Organization Chair). Program Comittee: ISSTA 2019, ICST 2019, ASE 2018, COMPSAC 2018, ICST 2018, ICSE SRC 2018, ISSTA Demo 2018, ICSE SRC 2017, EIT 2017, RE 2017, ICST 2016, ICST Tool Track 2016, ICISS 2016. Reviewer for: IEEE Transactions on Software Engineering (TSE); IEEE Transactions on Software Reliability (TR); Transactions on Embedded Computing Systems (TECS); Journal of Software Testing, Verification and Reliability (STVR); Journal of Systems and Software (JSS); Journal of Empirical Software Engineering (EMSE) Journal of Software: Evolution and Process (JSME); Journal on Software and Systems Modeling (SoSyM); Frontier of Computer Science (FCS).