

Meikang Qiu

Meikang Qiu, Ph.D.

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Education and Training

Ph.D. Computer Science, University of Texas at Dallas, TX
M.S. Computer Science, University of Texas at Dallas, TX
M.Eng. Engineering, Shanghai Jiaotong University, Shanghai, China,
B.Eng. Engineering, Shanghai Jiaotong University, Shanghai, China,

Research and Professional Experience:

2009- present	Assistant Professor	Dept. of Elec. and Comp. Eng. (Tenure track)	University of Kentucky, KY
2007- 2009	Assistant Professor	Dept. of Elec. and Comp. Eng. (Tenure track)	University of New Orleans, LA

Five Closely Related Publications

1. **M. Qiu** and E. H.-M. Sha, *Cost Minimization while Satisfying Hard/Soft Timing Constraints for Heterogeneous Embedded Systems*, in *ACM Transactions on Design Automation of Electronic Systems (TODAES)*, Vol. 14, No.2, Article 15, pp. 1-30, Mar. 2009.
2. **M. Qiu**, L. T. Yang and E. H.-M. Sha, *Rotation Scheduling and Voltage Assignment to Minimize Energy for SoC*, in *IEEE CSE+EUC*, pp. 48-55, Vancouver, Canada, Aug. 2009 (**Best Paper Award**).
3. **M. Qiu**, L. T. Yang, Z. Shao, E. H.-M. Sha, *Dynamic and Leakage Energy Minimization with Soft Real-Time Loop Scheduling and Voltage Assignment*, in *IEEE Transactions on Very Large Scale Integration Systems (TVLSI)*, Vol. 18, No.3, pp. 501-504, Mar. 2010
4. **M. Qiu**, Z. Jia, C. Xue, Z. Shao and E. H.-M. Sha, *Voltage Assignment with Guaranteed Probability Satisfying Timing Constraint for Real-time Multiprocessor DSP*, in *Journal of Signal Processing Systems (JSPS)*, Vol. 46, No. 1, pp. 55-73, Jan. 2007.
5. **M. Qiu**, E. H.-M. Sha, M. Liu, M. Lin, S. Hua and L. T. Yang, *Energy Minimization with Loop Fusion and Multi-Functional-Unit Scheduling for Multidimensional DSP*, in *Journal of Parallel and Distributed Computing (JPDC)*, Vol. 68, No. 4, pp 443-455, Apr. 2008.

Five Other Related Publications

1. **M. Qiu**, C. Xue, Z. Shao, and E. H.-M. Sha, *Energy Minimization with Soft Real-time and DVS for Uniprocessor and Multiprocessor Embedded Systems*, in *IEEE/ACM Design, Automation and Test in Europe (DATE)*, pp. 1641-1646, Acropolis, Nice, France, Apr. 2007
2. **M. Qiu**, L. Zhang, and E. H.-M. Sha, *ILP Optimal Scheduling for Multi-Module Memory*, in *ACM/IEEE CODES+ISSS*, pp. 277-286, Grenoble, France, Oct. 2009.
3. **M. Qiu** and J. Wu, *Loop Scheduling and Assignment with Prefetching to Minimize Energy while Hiding Memory Latencies*, in *ACM GLSVLSI*, Orlando, Florida, May 2008.
4. **M. Qiu**, H. Li, E. H.-M. Sha, *Heterogeneous Real-Time Embedded Software Optimization Considering Hardware Platform*, in *ACM SAC*, pp. 1637-1641, Honolulu, Hawaii, Mar. 2009
5. Z. Shao, C. Xue, Q. Zhuge, **M. Qiu**, B. Xiao and E. H.-M. Sha, *Security Protection and Checking for Embedded System Integration Against Buffer Overflow Attacks via Hardware/Software*, in *IEEE Transactions on Computers*, Vol. 55, No. 4, pp. 443-453, Apr. 2006.

Synergistic Activities:

- Conference chair, program committee, and reviewer:
 - Program Chair of The Fourth International conference on Embedded and Multimedia Computing, Jeju, Korea, Dec. 10-12, 2009 (EM-Com 2009).
 - Publication Chair of the 15th IEEE International Conference on Embedded and Real-Time Computing Systems and Applications, Beijing, China, Aug. 2009 (RTCSA 2009).
 - Publicity Chair, The Fifth IEEE International Symposium on Embedded Computing, Beijing, China, October 6-9, 2008 (SEC 2008),
 - Vice general chair of the 9th IEEE International Conference on Computational Science and Engineering (CSE 2008)
 - Program committee member and session chair for IEEE Globecom 2008, PDCS 2007, RTCSA 2009, ICESS 2008, ICESS 2009, EUC 2009
 - Reviewer for CODES+ISSS, IEEE Trans. on Dependable and Secure Computing, IEEE Trans. on Computer, IEEE Trans. on VLSI Systems, and ACM TODAES
- IEEE senior member, ACM member

Graduate Advisor:

Professor Edwin H.-M. Sha (Computer Science, University of Texas at Dallas)

Graduate Students:

Jiayin Li (Ph.D. student, University of Kentucky)

Jianning Liu (Ph.D. student, University of Kentucky)